



Sandip Foundation's
Sandip Institute of Engineering and Management,
Mahiravani, Trimbak Road, Nashik-422213
Phone: (02594) 222 581/82/83/84, Fax: (02594) 222 585
<http://www.sandipfoundation.org> / info@siem.org.in



Event Title: Faculty Development Program on VLSI Design: Silicon to ICs.

Event Scheduled: 22nd – 26th July 2024 (10:00 AM- 4:00 PM)

Timing: 10:00 am onwards.

Event Venue: Online Mode

Participants: Faculties and Researchers from various institutes

Name of Faculty coordinator: Prof. Vinay Kr. Jain, Prof. Neha Niharika, Prof. Amit Kr. Mishra, faculty from E&TC department.

Objectives:

The objective of a Faculty Development Program (FDP) on VLSI Design: Silicon to ICs (Integrated Circuits) includes the following key points:

1. **Enhance Knowledge:** Update faculty members on the latest developments, tools, and techniques in VLSI (Very-Large-Scale Integration) design, from silicon fabrication to the creation of integrated circuits.
2. **Improve Teaching Skills:** Equip faculty with the necessary skills and knowledge to effectively teach VLSI design concepts, ensuring they can provide high-quality education to their students.
3. **Hands-on Experience:** Provide practical training on advanced VLSI design tools and software, enabling faculty to gain hands-on experience and better understand the design and fabrication process.
4. **Research Orientation:** Encourage and guide faculty members to pursue research in VLSI design, fostering a research-oriented mindset and enhancing their ability to contribute to academic and industrial advancements.
5. **Industry Collaboration:** Promote collaboration between academia and industry by exposing faculty to real-world VLSI design challenges and industry practices, helping them align their teaching and research with industry needs.

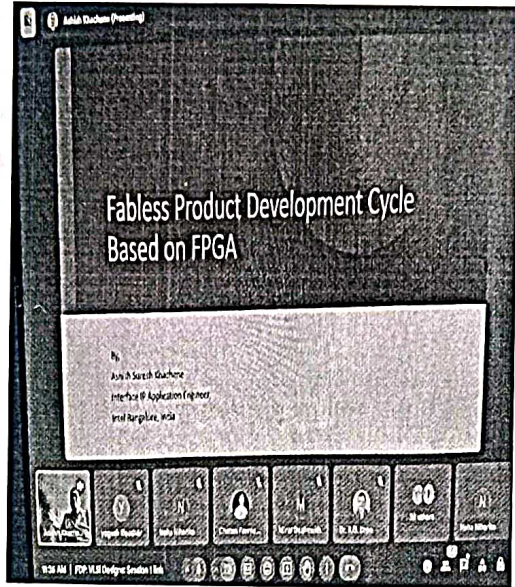
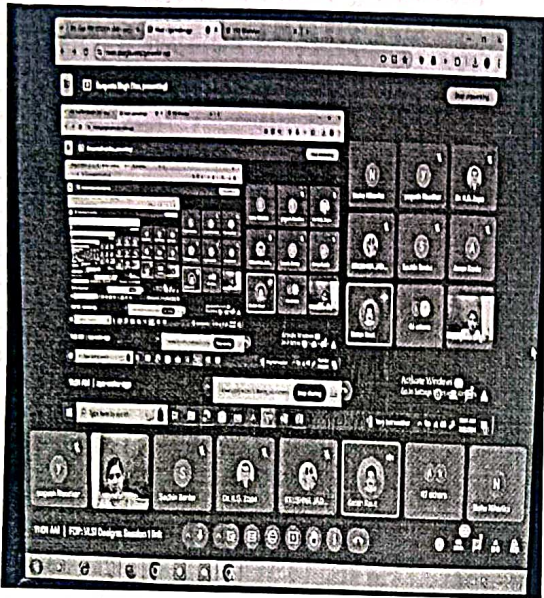


Photo

Photos of the Event

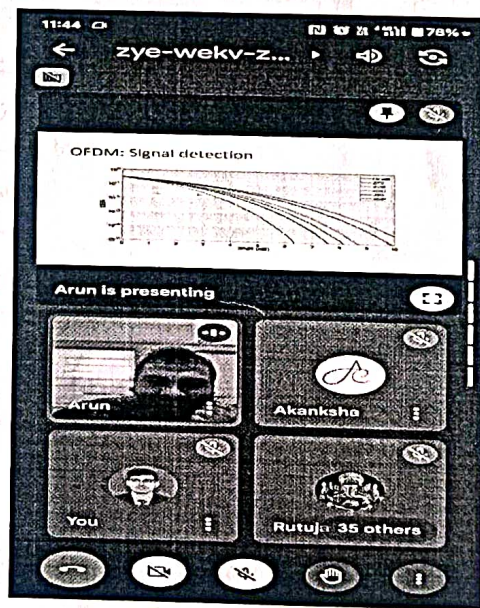
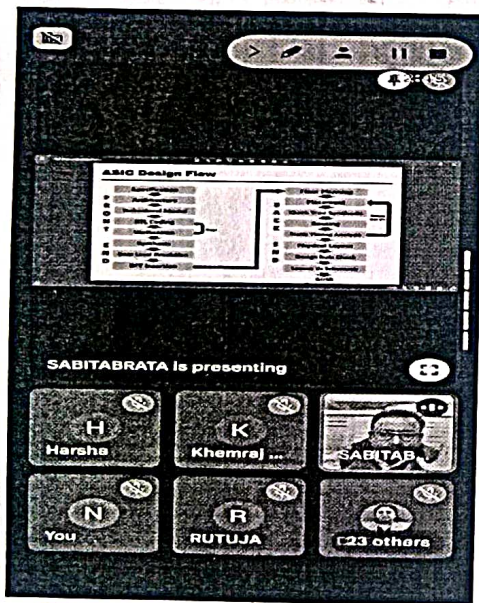
Day 1

Speakers: Dr. Sangeeta Singh, NIT Patna
Mr. Ashish Kachane, Intel Corporation
Dr. Sanjeev Sharma, NHCE



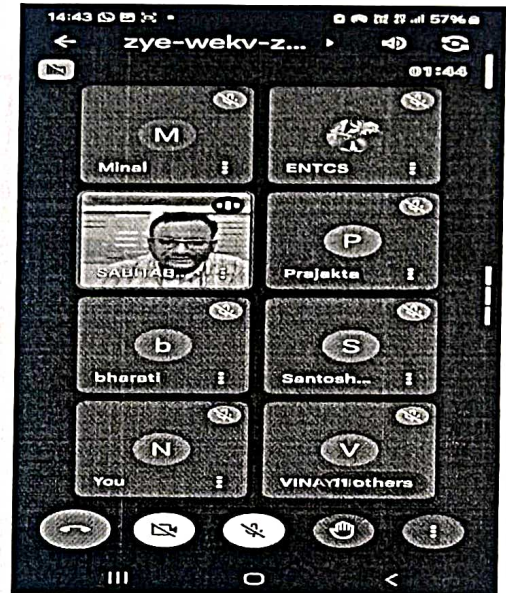
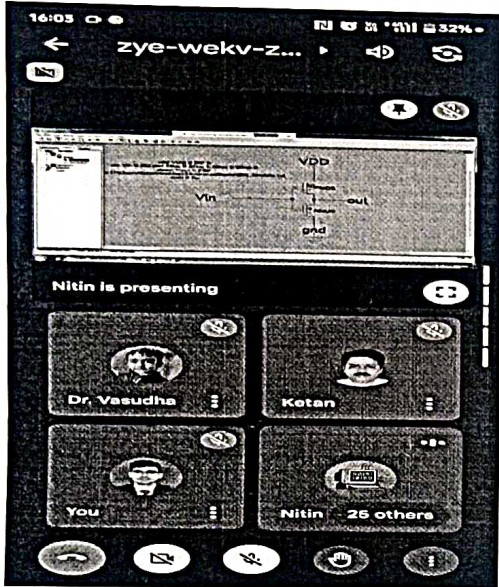
Day 2

Speakers: Mr. Arun Kumar, NHCE
Mr. Sabitabrata Bhattacharya, NHCE



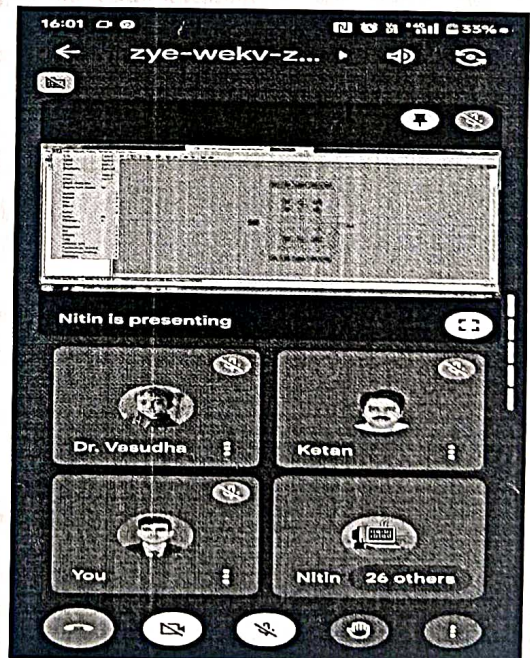
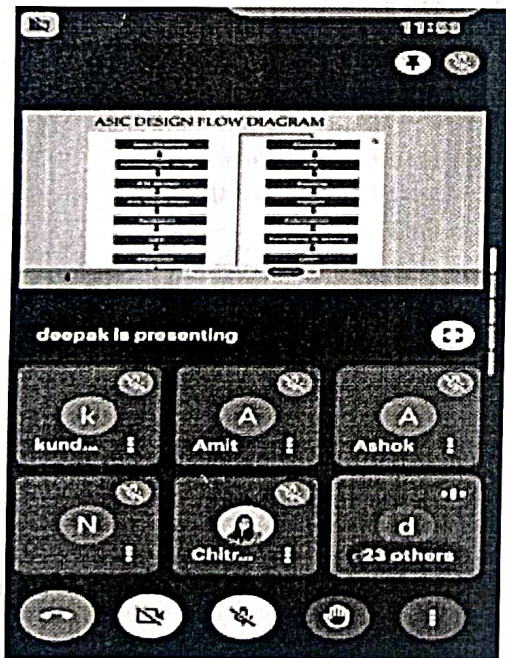
Day 3

Speakers: Mr. Sabitabrata Bhattacharya, NHCE
Mr. Nitin N R, NHCE



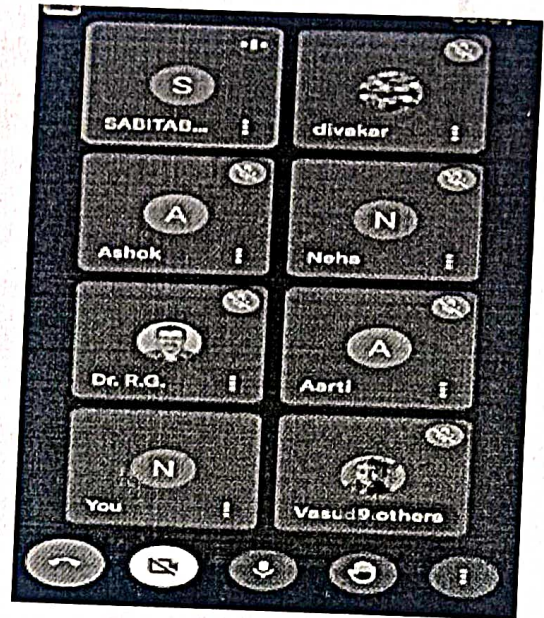
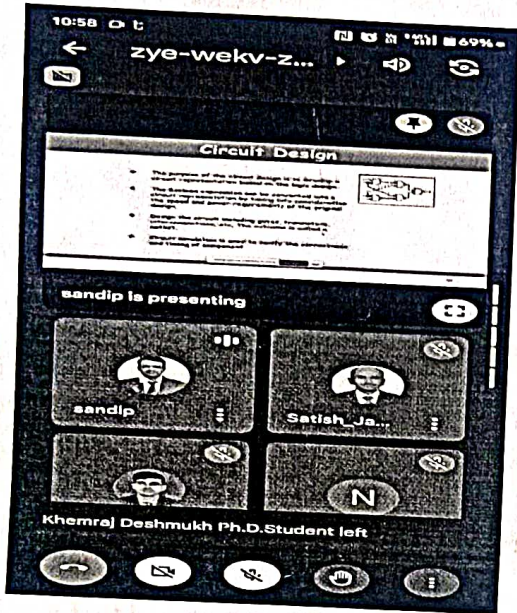
Day 4

Speakers: Mr. Deepak Nadiger, Cienra Techolutions
Mr. Nithin Kumar N R, NHCE



Day 5

Speakers: Mr. Sandeep Kakde, Cienra Techolutions
Mr. Sabitabrata Bhattacharya, NHCE



Dr. Vinay Kumar Jain

Prof. Neha Niharika

Prof. Amit Kumar Mishra

Event Coordinator

Prof. Y. R. Risodkar

H.O.D, E&TC

