

### About Institute

Sandip Institute of Engineering & Management (SIEM) is offering from the Sandip Foundation to the people of Nashik. SIEM is located in the scenic, Eco-friendly and conducive to study campus at an elevation off the Trimbak Road (Mahiravani, Nashik). SIEM is accredited by NACC. At SIEM, the focus is on interactive Teaching Learning, Industrial Projects and Training, Faculty empowerment, Industrial visits so that learning is applicable oriented and students develop the necessary skills. SIEM is committed to imparting quality education in an atmosphere that will ensure that its students are confident, self-motivated and industry ready. Towards this goal, we are giving importance to qualified and experienced faculty for effective teaching learning process, equipped our laboratories with best-in-class machines and instruments and developing overall personality of our students.

### Executive Committee

**Dr. N.L. Bhirud** (Dean Academics, SIEM)  
**Dr. L. K. Toke** (Dean Administration, SIEM)  
**Dr. S. K. Gade** (Dean R & D, SIEM)  
**Dr. A.S. Dube** (Head Mechanical Engineering, SIEM)  
**Prof. K.A. Salunkhe** (Head Civil Engineering, SIEM)  
**Prof. Y. R. Risodkar** (Head E & TC Engineering, SIEM)  
**Prof. R. K. Jha** (Head Electrical Engineering, SIEM)  
**Prof. R. J. Nayak** (Head Applied Science, SIEM)

### Organizing Committee

**Prof Mosam Sangole** (Asst. Prof., E&TC Engg.)  
**Prof Bharat Deore** (Asst. Prof., E&TC Engg.)  
**Prof. Pramod Aswale** (Asst. Prof., E&TC Engg.)  
**Prof. Amit K Mishra** (Asst. Prof., E&TC Engg.)  
**Prof. Sushant J Pawar** (Asst. Prof., E&TC Engg.)

### Resource persons

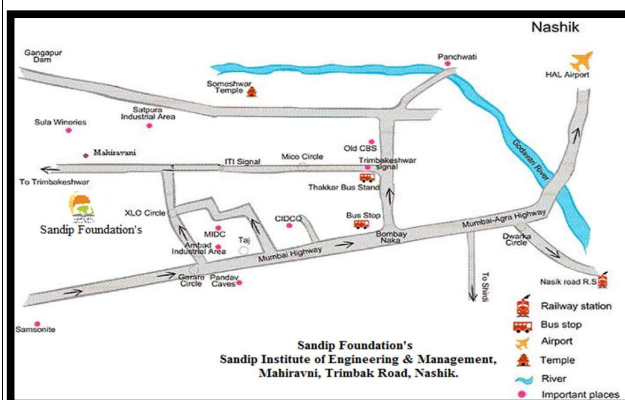
**Mr. Prashant Salunkhe,**  
Trainer

### Important Dates

Last date of receiving Registration.

**17<sup>th</sup> Aug 2019**

**Any Query Contact Organizing Coordinator**



### Communication Address

**SANDIP FOUNDATION'S**  
**SANDIP INSTITUTE OF ENGINEERING & MANAGEMENT,**  
**MAHIRAVANI, TRIMBAK ROAD TAL & DIST. NASHIK-**  
**422213, MAHARSHTRA**  
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### Value Addition Program on “Proteus S/W Software And PCB Designing”

**19<sup>th</sup> Aug 2019- 23<sup>rd</sup> Aug 2019**

### Organized by

**Department of Electronics And  
Telecommunication Engineering, SIEM**

### Chief Patron

**Dr. Sandip Kumar N. Jha**  
(Chairman, Sandip Foundation)

### Patron

**Prof. Pramod K. Karole**  
(Academic Facilitator, Sandip Foundation)  
**Dr. D. P. Patil**  
(Principal, SIEM)

### Convener

**Prof. Yogesh Risodkar**  
**Head, Department of Electronics And  
Telecommunication Engineering, SIEM**

### Coordinator

**Prof. Y.R Risodkar**  
**Asst Professor, SIEM**

## **Value Added Program on “ Proteus software: Simulation and PCB design”**

- 1. Event Title:** Value Added Program on “Proteus software: Simulation and PCB design”
- 2. Event Date:** 19<sup>th</sup> August 2019 to 23<sup>rd</sup> August 2019
- 3. Event Conduction Duration:** 5 days
- 4. Event Venue:** SE E&TC Classroom
- 5. Event Resource Person Details:** Representatives from Autonetics Pvt Ltd, Nasik
- 6. Name of Event Coordinator :** Prof Y RRisodkar
- 7. Expected Audience:** T.E
- 8. Number of Students:** 17

### **9. Event Objectives & Outcomes:**

- 1) The experts explained the students regarding the applications of Proteus software mainly by electronic design engineers and technicians to create schematics and electronic prints for manufacturing printed circuit boards.
- 2) They also guided regarding ISIS used to draw schematics and simulate the circuits in real time. The simulation allows human access during run time, thus providing real time simulation.
- 3) The installation of ARES which is used for PCB designing was also demonstrated exhibiting the feature of viewing output in 3D view of the designed PCB along with components.



## **Photos**







## Certificates:

 <b>Sandip Foundation's</b> <b>Sandip Institute of Engineering and Management, Nashik Maharashtra</b> <b>Department of Electronics and Telecommunication</b>	 
<b>Certificate of Participation</b>	
<p>This is to certify that <b>MAHALE AKASH SATISH</b> has successfully completed <b>Value Addition Program on "Proteus software: Simulation and PCB design"</b>. This Value Addition Program was scheduled from 19<sup>th</sup> August 2019 to August 2019.</p>	
 Prof Y R Risodkar HOD, ENTIC	 Prof (Dr) D P Patil Principal, SIEM

 <b>Sandip Foundation's</b> <b>Sandip Institute of Engineering and Management, Nashik Maharashtra</b> <b>Department of Electronics and Telecommunication</b>	 
<b>Certificate of Participation</b>	
<p>This is to certify that <b>JADHAV RAHUL LAXMAN</b> has successfully completed <b>Value Addition Program on "Proteus software: Simulation and PCB design"</b>. This Value Addition Program was scheduled from 19<sup>th</sup> August 2019 to August 2019.</p>	
 Prof Y R Risodkar HOD, ENTIC	 Prof (Dr) D P Patil Principal, SIEM

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Department of Electronics and Telecommunication



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### Certificate of Participation

This is to certify that **SHINDE YUTIKA** has successfully completed Value Addition Programs on Proteus software : **Schematic and PCB Design**. This Value Addition Program was conducted from 19<sup>th</sup> August 2019 to August 2019.

Prof Y R Risodkar  
HOD, ETC

Prof (Dr) D P Patil  
Principal, SIEM

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Department of Electronics and Telecommunication



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### Certificate of Participation

This is to certify that **RATHOD AKSHAY VIHAL** has successfully completed Value Addition Programs on "Proteus software : **Schematic and PCB Design**". This Value Addition Program was conducted from 19<sup>th</sup> August 2019 to August 2019.

Prof Y R Risodkar  
HOD, ETC

Prof (Dr) D P Patil  
Principal, SIEM

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**Sandip Foundation's**

**Sandip Institute of Engineering and Management, Nashik Maharashtra**

**Department of Electronics and Telecommunication**



## **Certificate of Participation**

This is to certify that RAJPUT POOJA JAYAPALSINGH has successfully completed Value Addition Program on "Proteus software: Simulation and PCB design". This Value Addition Program was scheduled from 19<sup>th</sup> August 2019 to August 2019.

**Prof Y R Risodkar**

HOD, ENT C

**Prof (Dr) D P Patil**

**Principal, SIEM**

## Registration:

Sandip Foundation's  
Sandip Institute of Engineering & Management, Nashik  
Department of Electronics and Telecommunication Engineering

A.Y. -2019-20 Class: SE Semester - II  
Event Name: VALUE ADDITION PROGRAM on "PROTEUS SW SIMULATION & PCB DESIGNING"  
Duration: 5 DAYS

Roll No	Candidate Name	Sign
1	AHIRE RITU VINAYAK	Ritu
2	BIRARIS CHETANA HEMRAJ	Bchdse
3	BORSE KANCHAN DINKAR	Kanchan
4	CHAUGHULE SWARNIL SANJAY	Swarnil
5	GHUGE VAISHNAVI RAJESH	Gh
6	PATIL PRAJAKTA LAXMAN	Ppatil
7	PHATANGARE SURAJ SUBHASH	SSP
8	RAJPUT POOJA JAYAPALSINGH	Pooja
9	RATHOD AKSHAY VITTHAL	Akshay
10	SUYOG JAYKUMAR CHAUDHARY	Suyog
11	SHINDE YUTIKA PRADIPRAO	Yutika
12	GAIKWAD KHUSHAL YASHWANT	Gaikwad
13	GOSAVI POOJA VIJAY	Gosavi
14	JADHAV RAHUL LAXMAN	Rahul
15	MAHALE AKASH SATISH	Aakash
16	PATHAN MOHAMMAD ZAID SHAHID	A

17	PATIL PURVAL YOGESH	Purval
18	SAYED MOHD KAIF	Mohd Kaif

*Y. R. Risodkar*

Prof Y R Risodkar  
HOD E & TC